

AUG 02 2007

Customer No.: 31561  
Docket No.: 13133-US-PA  
Application No.: 10/711, 627AMENDMENT

Please amend the application as indicated hereafter.

To the Claims:

1. (currently amended) A liquid crystal display panel, comprising:
  - a color filter substrate;
  - a thin film transistor array substrate, disposed parallel to the color filter substrate, wherein the thin film transistor array substrate has a plurality of gate layer lines and a plurality of source layer lines thereon;
  - a plurality of spacers, disposed on the color filter substrate, wherein the spacers at least lean on one of the side edges of the gate layer lines or the source layer lines, and the spacers are disposed at the overlapping area between the gate layer lines and the source layer lines with the spacers leaning against both the gate layer lines and the source layer lines, wherein adjacent spacers are joined together on the color filter substrate to form a latching structure such that a central recess section of the latching structure is able to latch onto the overlapping area between the gate layer lines and the source layer lines; and
  - a liquid crystal layer, disposed between the color filter substrate and the thin film transistor array substrate.
2. (original) The liquid crystal display panel of claim 1, wherein the gate layer lines comprise scan lines and the source layer lines comprise data lines.
3. (original) The liquid crystal display panel of claim 2, wherein the gate layer lines further comprise common lines.

Customer No.: 31561  
Docket No.: 13133-US-PA  
Application No.: 10/711, 627

4-9. (cancelled)

10. (original) The liquid crystal display panel of claim 1, wherein the spacers have a cylindrical or a wall-like shape.

11-14. ( cancelled)

15. (new) A liquid crystal display panel, comprising:  
a color filter substrate;  
a thin film transistor array substrate, disposed parallel to the color filter substrate, wherein the thin film transistor array substrate has a plurality of gate layer lines and a plurality of source layer lines thereon;

a plurality of spacers, disposed on the color filter substrate, wherein the spacers at least lean on one of the side edges of the gate layer lines or the source layer lines, and the spacers are disposed on opposite sides of the gate layer lines with the spacers leaning against the gate layer lines as well, wherein adjacent spacers are joined together on the color filter substrate to form a latching structure such that a central recess section of the latching structure is able to latch onto the gate layer lines; and

a liquid crystal layer, disposed between the color filter substrate and the thin film transistor array substrate.

16. (new) The liquid crystal display panel of claim 15, wherein the gate layer lines comprise scan lines and the source layer lines comprise data lines.

17. (new) The liquid crystal display panel of claim 16, wherein the gate layer lines further comprise common lines.

18. (new) The liquid crystal display panel of claim 15, wherein the spacers have a cylindrical or a wall-like shape.

Customer No.: 31561  
Docket No.: 13133-US-PA  
Application No.: 10/711, 627

19. (new) A liquid crystal display panel, comprising:

a color filter substrate;

a thin film transistor array substrate, disposed parallel to the color filter substrate, wherein the thin film transistor array substrate has a plurality of gate layer lines and a plurality of source layer lines thereon;

a plurality of spacers, disposed on the color filter substrate, wherein the spacers at least lean on one of the side edges of the gate layer lines or the source layer lines, and the spacers are disposed on opposite sides of the source layer lines with the spacers leaning against the source layers as well, wherein adjacent spacers are joined together on the color filter substrate to form a latching structure such that a central recess section of the latching structure is able to latch onto the source layers; and

a liquid crystal layer, disposed between the color filter substrate and the thin film transistor array substrate.

20. (new) The liquid crystal display panel of claim 19, wherein the gate layer lines comprise scan lines and the source layer lines comprise data lines.

21. (new) The liquid crystal display panel of claim 20, wherein the gate layer lines further comprise common lines.

22. (new) The liquid crystal display panel of claim 19, wherein the spacers have a cylindrical or a wall-like shape.